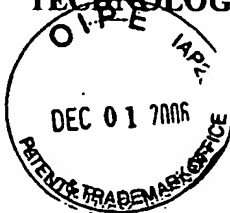


First Named Inventor	Scott Derner	REPLY UNDER 37 C.F.R. §1.116 – EXPEDITED PROCEDURE – TECHNOLOGY CENTER 2100 
Serial No.	10/022,010	
Filing Date	December 13, 2001	
Group Art Unit	2133	
Examiner Name	Joseph D. Torres	
Confirmation No.	2780	
Attorney Docket No.	400.119US01	
Title: ERROR CORRECTION IN ROM EMBEDDED DRAM		

Mail Stop: AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed October 2, 2006, please consider the following:

ok to enter
12/15/06